[**3-Bit BCD Display**](https://www.youtube.com/watch?v=V7LNtxWiSOc&list=PLk4sSigu0N0W4v755N_O6Jk1WWrfWIGgm&index=12)**:**

This section comprises of  a Binary to Decimal converter along with 3 seven-segment display.The converter is not necessary. From the output coming from output register , we can represent numbers upto 2^8.

A screenshot of a computer

Description automatically generated

When result is 10 , it will show the hex equivalent that is A and so on, But the converter will make the display show Decimal values.As a result we will be able to show from 000-255 in thoes displays.

The converter is made using double dabble method whose explanation can be found in google and youtube videos i.e here [(i)](https://www.youtube.com/watch?v=eXIfZ1yKFlA) [(ii)](https://www.youtube.com/watch?v=kusZDF3IfBA).

A diagram of a computer

Description automatically generated

Each sub-circuit is the same and the combination of logic gates inside actually comes from double method implementation.

A diagram of a computer

Description automatically generated

This is the input output we required which was done in circuits.

A paper with a number on it

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